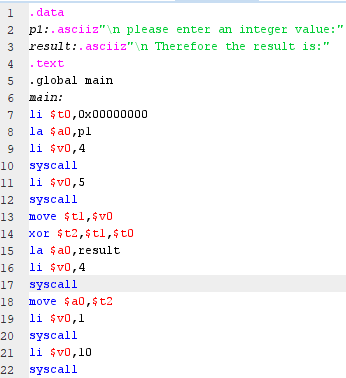
*LAB # 11*

*lab task*

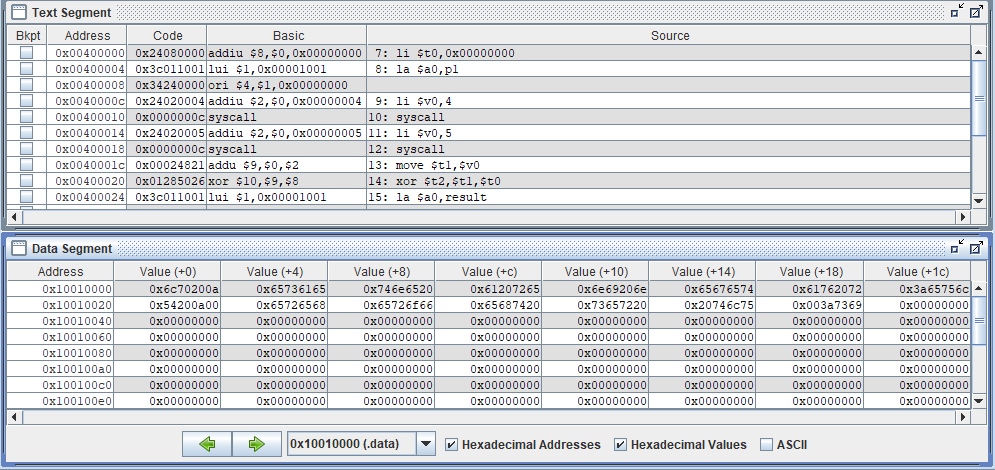
1. Write a MIPS assembly program that take input value and perform bitwise XOR

instruction with mask 0.

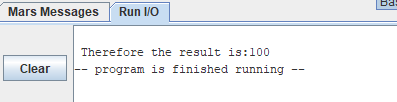
**CODE:**

****

**EXECUTION:**

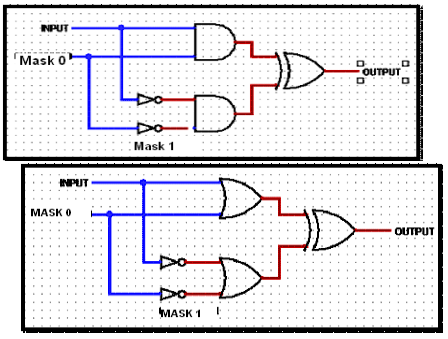
****

**OUTPUT:**

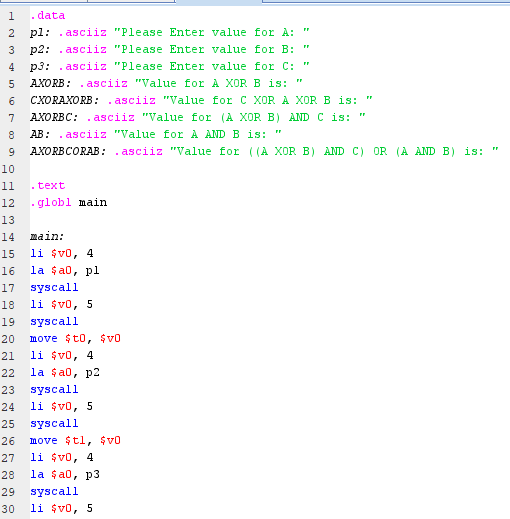
****

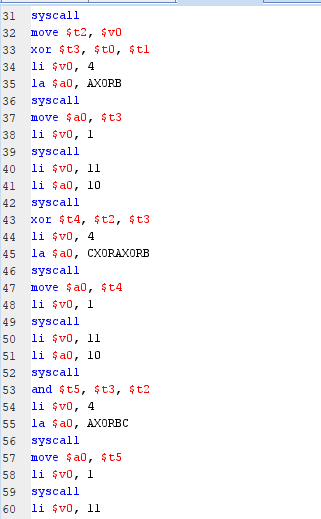
2. Implement following circuits into MIPS Assembly Language using AND, OR

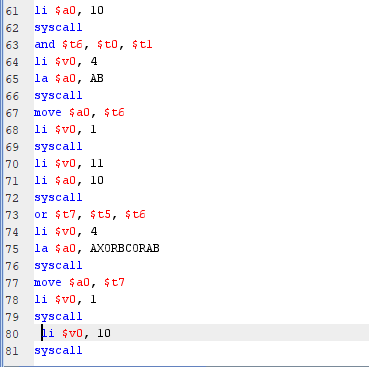
& XOR instructions



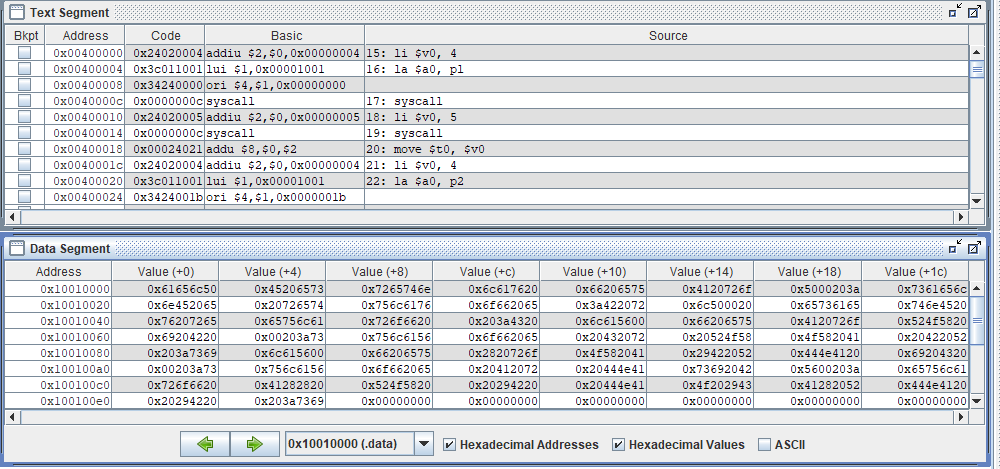
**CODE:**

****

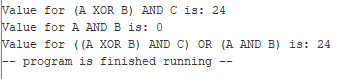
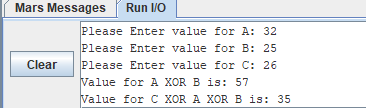
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**EXECUTION:**

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**OUTPUT:**

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